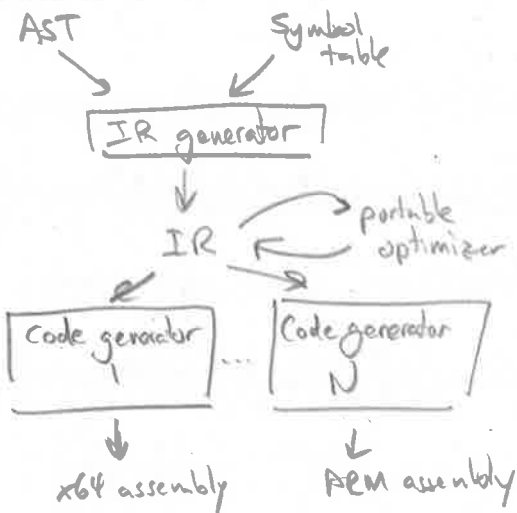


# Intermediate-code generation pg-1

## Lecture topics

- I. Intermediate representation
- II. Expressions
- III. Statements
- IV. Jumping code
- V. Calls and returns
- VI. Memory accesses
- VII. Literals

## I. Intermediate representation



### Variety of choices for IR:

- 3-address code
- SSA form
- stack-based vs. register-based

### This class:

- 3-address code
- "infinite" number of virtual registers

IR for TACK: see handout.

## II. Expressions

Example TACK:

$$x = (y + 2) * z$$

IR:

$$\begin{aligned}
 t_1 &= y + 2 \\
 t_2 &= t_1 * z \\
 x &= t_2
 \end{aligned}$$

Idea: use synthesized attributes  
code, addr

## SDD (see Dragon book Fig. 6.19 / p. 379)

$S \rightarrow ID = E.$	$s.code = E.code \mid ID = E.addr$
$E \rightarrow E_1 \text{ infixOp } E_2$	$E.addr = \text{new TempAddr}()$ $E.code = E_1.code \parallel E_2.code$ $\parallel E.addr = E_1.addr \text{ infixOp } E_2.addr$
$E \rightarrow \text{prefixOp } E_1$	$E.addr = \text{new TempAddr}()$ $E.code = E_1.code$ $\parallel E.addr = \text{prefixOp } E_1.addr$
$E \rightarrow (E_1)$	$E.addr = E_1.addr$
$E \rightarrow ID$	$E.addr = \text{new NameAddr}(ID)$

## III. Statements

Example TACK:

```

if x < 0
  y := 1;
else
  y := 2;
z = y;
  
```

IR

```

if false goto l2.
l1: y = 1;
goto l3
l2: y = 2
l3: z = y
  
```

Idea: use inherited attributes for labels

AST Node	Attribute	Example
stmt	next	$l_3$
Expr (boolean)	true, false	$l_1, l_2$

Intermediate Code generation pg. 2

SDD (see Dragon book Fig. 6.36 / p. 402)

$B \rightarrow \{S_1, \dots, S_n\}$

$S_1.next = \text{new Label}()$   
 ...  
 $S_{n-1}.next = \text{new Label}()$   
 $S_n.next = B.next$   
 $B.code = S_1.code$   
 ||  $S_1.next = S_2.code$   
 || ...  
 ||  $S_{n-1}.next = S_n.code$

$S \rightarrow B | I | W | A$

$I \rightarrow \text{if } E \text{ } B_1 \text{ else } B_2$

$E.true = \text{new Label}()$   
 $E.false = \text{new Label}()$   
 $B_1.next = B_2.next = I.next$   
 $I.code = E.code$   
 ||  $E.true : B_1.code$   
 || goto  $I.next$   
 ||  $E.false : B_2.code$

$W \rightarrow \text{while } E \text{ } B$

$E.true = \text{new Label}()$   
 $E.false = W.next$   
 $B.next = \text{new Label}()$   
 $W.code = B.next : E.code$   
 ||  $E.true : B.code$   
 || goto  $B.next$

IV Jumping code

Two ways to translate boolean expressions

	Jumping Code	Value Code
Context	Control statement other boolean expression	Assignment other non-boolean expression
Attributes	true/false label (inherited) code (synthesized)	addr (synthesized) code (synthesized)

Example TACIC

if  $a \&\& b == 0$  {  
 $y := 1$ ;  
 } else {  
 $y := 2$ ;  
 }  
 $z = y$

IR

if false goto  $l_2$   
 if  $b == 0$  goto  $l_1$   
 goto  $l_2$   
 $l_1: y = 1$   
 goto  $l_3$   
 $l_2: y = 2$   
 $l_3: z = y$

SDD (see Dragon book Fig. 6.37 / p. 404)

$E \rightarrow E_1 \parallel E_2$

$E_1.true = E.true$   
 $E_1.false = \text{new Label}()$   
 $E_2.true = E.true$   
 $E_2.false = E.false$   
 $E.code = E_1.code$   
 ||  $E_1.false : E_2.code$

$| E_1 \&\& E_2$

$E_1.true = \text{new Label}()$   
 $E_1.false = E.false$   
 $E_2.true = E.true$   
 $E_2.false = E.false$   
 $E.code = E_1.code$   
 ||  $E_1.true : E_2.code$

$| ! E_1$

$E_1.true = E.false$   
 $E_1.false = E.true$   
 $E.code = E_1.code$

$| E_1 \text{ rel op } E_2$

$E.code = E_1.code$   
 ||  $E_2.code$   
 || if  $E_1.addr \text{ rel op } E_2.addr$  goto  $E.true$   
 || goto  $E.false$

$| true$

$E.code = \text{goto } E.true$

$| false$

$E.code = \text{goto } E.false$

Sometimes, need to convert:

given	needed	example
jumping code	value code	$x = y \parallel z$
value code	jumping code	if $x \&\& y \{ \dots \}$

Given jumping code, generate value code:

$E \rightarrow \dots$

$E.true = \text{new Label}();$   
 $E.false = \text{new Label}();$   
 $jcode = \text{genJumpingCode}(E)$   
 $E.addr = \text{new TempAddr}();$   
 $E.code = E.addr = true$   
 ||  $jcode$   
 ||  $E.false : E.addr = false$   
 ||  $E.true:$

Given value code, generate jumping code

$E \rightarrow \dots$

$vcode = \text{genValueCode}(E)$   
 $E.code = vcode$   
 || if  $E.addr$  goto  $E.true$   
 || goto  $E.false$

V calls and returns

Example TACK

$x = f(E_0, \dots, E_{n-1})$

IR

$E_0.code$   
 $\dots$   
 $E_{n-1}.code$   
 $param[0:n] = E_0.addr$   
 $\dots$   
 $param[n-1:n] = E_{n-1}.addr$   
 $x = call\ f:n$

Example TACK

$\rightarrow E;$

IR

$E.code$   
 $return\ E.addr$

VI Memory accesses

Two ways to translate array / record accesses

	L-value	R-value
Context	left-hand-side of assignment	anywhere else
Attributes	addr refers to storage location	addr refers to storage contents
Dereference	no	yes

TACK has "array of arrays";  
 not true "multi-dimensional arrays".  
 easier to translate than in Dragon book

Example TACK

$x = a[i][j][k];$

IR

$t_1 = a[i]$   
 $t_2 = t_1[j]$   
 $t_3 = t_2[k]$

$a[i][j][k] = x;$

$t_1 = a[i]$   
 $t_2 = t_1[j]$   
 $t_2[k] = x$

$x = r.f.g.h$

$t_1 = r.f$   
 $t_2 = t_1.g$   
 $x = t_2.h$

$r.f.g.h = x;$

$t_1 = r.f$   
 $t_2 = t_1.g$   
 $t_2.h = x$

SAD

$S \rightarrow IO = E$

$S.code = E.code$   
 $||\ IO = E.addr$

$| E_1[E_2] := E_3$

$S.code = E_3.code$   
 $||\ E_1.code$   
 $||\ E_2.code$   
 $||\ E_1.addr[E_2.addr]$   
 $= E_3.addr$

$| E_1.IO := E_2$

$S.code = E_2.code$   
 $||\ E_1.code$   
 $||\ E_1.addr.IO = E_2.addr$

$E \rightarrow IO$

$E.addr = new\ NameAddr(IO)$

$| E_1[E_2]$

$E.addr = new\ TempAddr()$   
 $E.code = E_1.code$   
 $||\ E_2.code$   
 $||\ E.addr = E_1.addr[E_2.addr]$

$| E.IO$

$E.addr = new\ TempAddr()$   
 $E.code = E_1.code$   
 $||\ E.addr = E_1.addr.IO$

VII Literals

Example TACK

$a = [E_0, \dots, E_{n-1}]$

IR

$param[0:2] = sizeof(\dots)$   
 $param[1:2] = n$   
 $a = call\ newArray\ '2$   
 $E_0.code$   
 $a[0] = E_0.addr$   
 $\dots$   
 $E_{n-1}.code$   
 $a[n-1] = E_{n-1}.addr$

Example TACK

$r = (IO_0 = E_0, \dots, IO_{n-1} = E_{n-1})$

IR

$param[0:1] = sizeof(\dots)$   
 $r = call\ newRecord;$   
 $E_0.code$   
 $r.IO_0 = E_0.addr$   
 $\dots$   
 $E_{n-1}.code$   
 $r.IO_{n-1} = E_{n-1}.addr$